1. **Name** : Prof. Vishwanath Shidlingappanavar
   Mobile: +91 - 7795702029
   Email : sishwanath_ec@secab.org
   Date of birth : 06.12.1989

2. **Designation** : Asst. Prof. in Electronics and Communication Engineering

3. **Degrees obtained** :

University	Degree	Year	Field of Specialization
Visvesvaraya Technology University	B.E.	2011	Electronics and Communication Engineering
Manipal University	M. Sc. Tech.	2014	VLSI Design

4. **Employment record (starting from present position) :**

University/College	Designation	Period	
		From	To
1. SECAB Institute of Engineering and Technology, Vijayapur	Assistant Professor	July 2017	Till Date

5. **Other related experience-research, industrial etc.**

Organization / Employer	Designation	From	To
1. Isoft Technologies, Mangalore	Intern	June 2013	June 2014
2. Winglobal Tek, Bangalore	FPGA Design Engineer	May 2015	June 2016

6. **Consultancy and Sponsored research activities :**

Year.	Organization.	Project Title.	Amount Received
2017	Karnataka State Council for Science and Technology, Govt. of Karnataka.	Automated Water Billing for Apartments	Rs.6000/-

7. *Publications : NIL*

8. **Conferences:** NIL

9. **Membership of Academic bodies, Scientific and Professional Societies**: NIL

10. **Awards:** Awarded for best project for the year 2017-18 in SECAB Institute of Engineering and Technology Vijayapur.

11. **No. of students guided:** B.Tech - 04

12. **List of Conferences, Short-term Courses etc. organized / participated**

ORGANIZED			
Sr.	Particulars.	Dates.	At.
1	One week Workshop on "PC Hardware and Networking" **under ISTE**	23rd July – 29th July	SECAB I.E.T, Vijayapur.
PARTICIPATED			
1	One day Workshop on "Excelling in Academics through IEEE"	2nd February 2018	BLDEACET, Vijayapur
2	One Week Faculty Development Programme on "Network Simulator NS2 and Network Security	5th to 10th February 2018	BBLDEACET, Vijayapur